## VALVE ELECTRONIC CV 1618

## GENERAL POST OFFICE: E-IN-C (W )

(POVT 54)

Specification:	G.P.O./CV1618/Issue 2	SECURITY			
Dated:	18.6.47	Specification	Valve		
To be read in o	conjunction with K 1001	Restricted	Restricted		

\_\_\_\_\_\_ indicates a change

TYPE OF VALVE: Transmitting CATHODE: Directly head ENVELOPE: Unmetallised PROTOTYPE	ted tungst	MARKING See Kl001/4 Additional markings required (See Notes A, B & Serial No		
Filament voltage Nominal filament current Max. anode voltage Max. anode dissipation Amplification factor Mutual conductance Anode impedance	(V) (A) (kV) (II) (mA/V) (ohms)	As Marked 10.0 3.0 250.0 7.0 1.2 6,000	Note  B  D  D  D	BASE See drawing on page 3.  CONNEXIONS See drawing on page 3.  DIMENSIONS See drawing on page 3.  PACKING See Klool/7.3
A. The Serial Numbers of B. The Marked Voltage of C. It is not essential D. Measured with Va = 1	is defined that the	d on page	oy the In 2, test al marki	

The tests shown in Table I, or alternatively, those shown in Table II, shall be performed in addition to those applicable in KlOOl.

Table I (for A.C. filament heating)

	TES	et condi	TIONS		TEST		LIMITS			
	Vf(V)	Va(V)	Vg(V)	Ia(mA)			Min.	Max.	No. Tested	Note
(a)	Read	20C	200	-	Vf required to an emission cu 250 mA. To be known as Voltage	rent of	11.0	13.0	100%	1
(b)	MV	•••	-	-	If	(A)	9.0	11.0	100%	
(c)	MV	3000	Adjust	84	Reverse Ig	(Au)	-	20.0	100%	2
(đ)	MV	1000 2000	Adjust	125	μ		6.3	7.7	100%	
(e)	MV	1000	Read	100	Vg .	(V)	-8.0	-28.0	100%	

Table II (for D.C. filament heating)

	TEST CONDITIONS				TEST	LIMITS			
	Vf(V)	Va(V)	Vg(V)	Ia(mA)	·	Min.	Max.	No. Tested	Note
(a)	Read	200	200	-	Vf required to produce an emission current of 250mm/ To be known as "Marked Voltage" (V)	11.0	13.0	. 100%	1
(ъ)	м.ч	_	-	-	If (A)	9.0	11.0	100%	,
(c)	M.V	3000	Adjust	84	Reverse Ig (pA)	-	20.0	100%	2
(a)	V.N	1000 2000	Adjust	125	μ	6.3	7.7	100%	
(e)	M.V	1000	Read	100	Vg (V)	-2.0	-22.0	100%	

## NOTES

- 1. This test shall be performed in accordance with Klool/AV
- 2. The duration of test (c) shall be 15 minutes and the reverse grid current shall not be rising at the end of the test.

## OUTLINE DRAWING

